

S13	4	(I/O) same (pad cell) same (system Group) same (switch\$4 adj current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:10
S14	770	(I/O) same (pad cell) same (system Group) same (position\$4 location)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:10
S15	24	(I/O) and (cell pad) same (group) same (chip layout) same placement same current and (switch\$4)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:15
S16	3	(I/O) same (cell pad) same (group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:17
S17	2	(I/O) same (cell pad) same (group) same (chip layout) same placement same (switch\$4 current)same control\$4 same file	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:18
S18	1	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)same control\$4 same file and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:19
S19	2	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:19
S20	4	(I/O) same (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:26
S21	39	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:28
S22	3	(I/O adj pad) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:29
S23	39	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current)	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:29
S24	22	(I/O) and (cell pad) same (set group) same (chip layout) same placement same (switch\$4 current) and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; DERWENT	OR	ON	2005/06/13 17:36